

Fig. 1 (Prior Art)

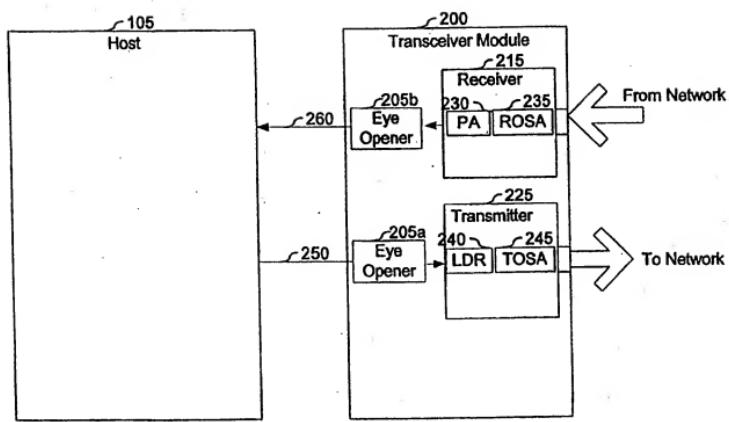


Fig. 2

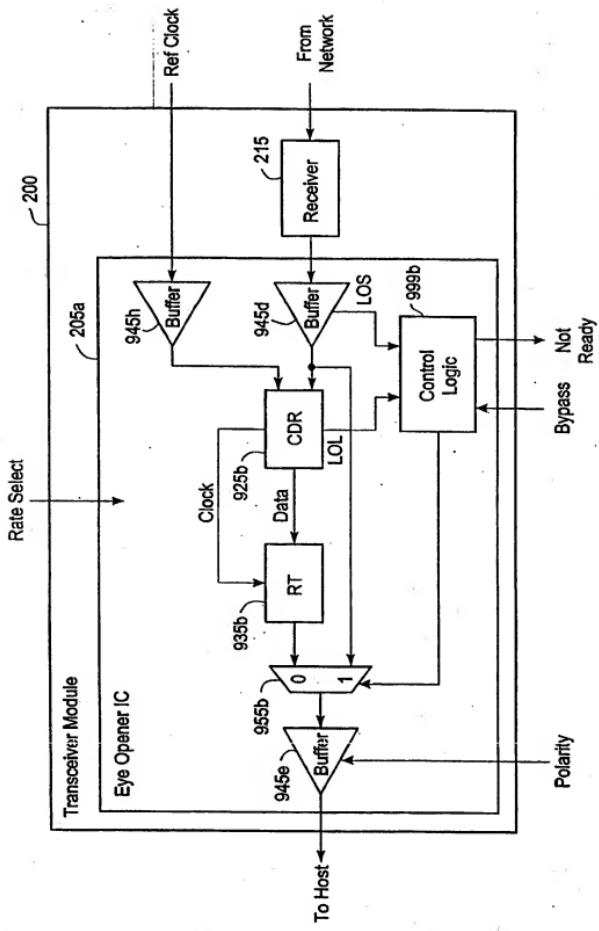


Fig. 3

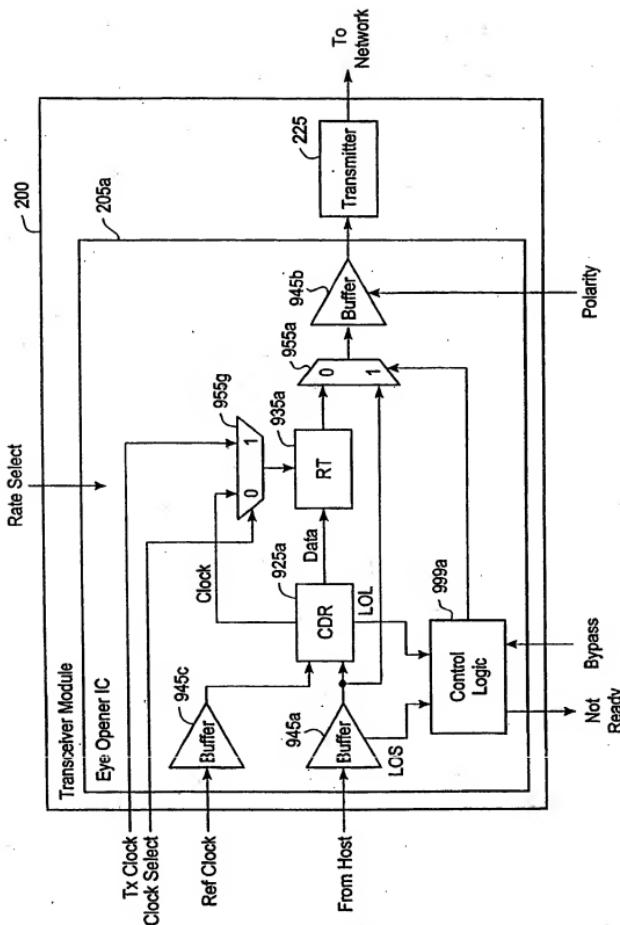


Fig. 4

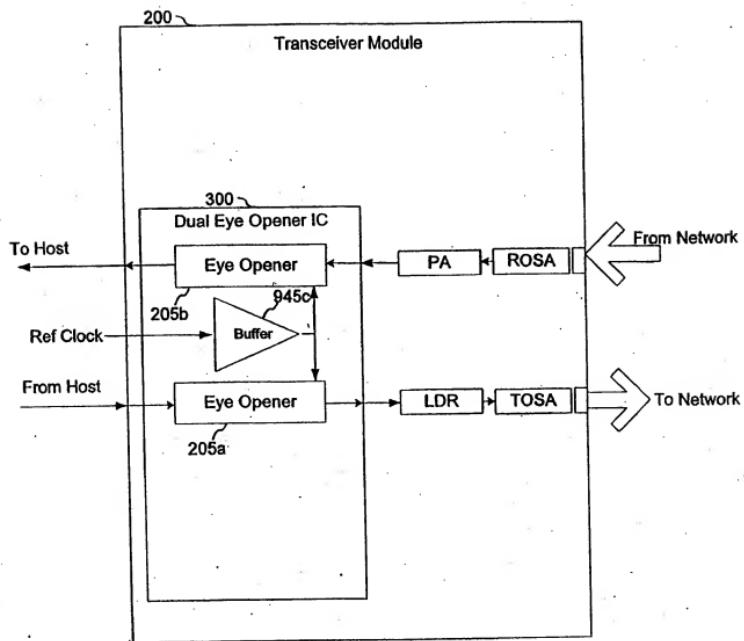


Fig. 5

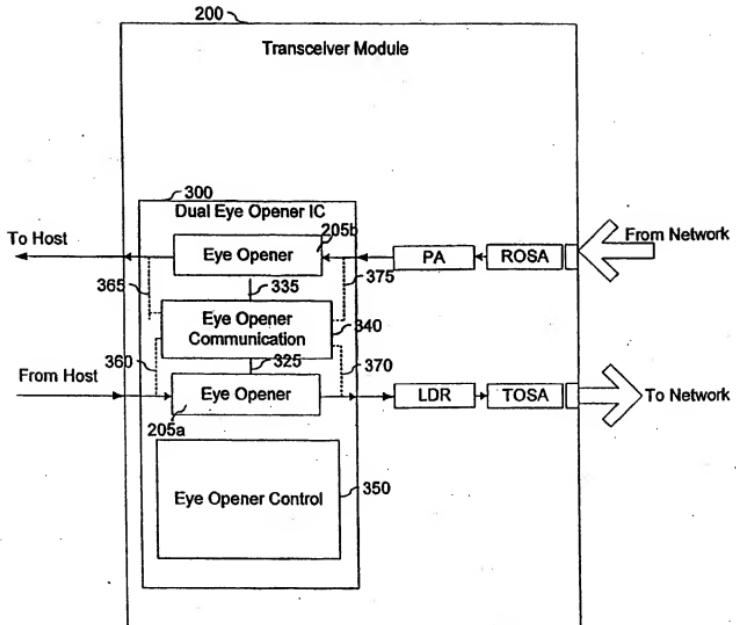


Fig. 6

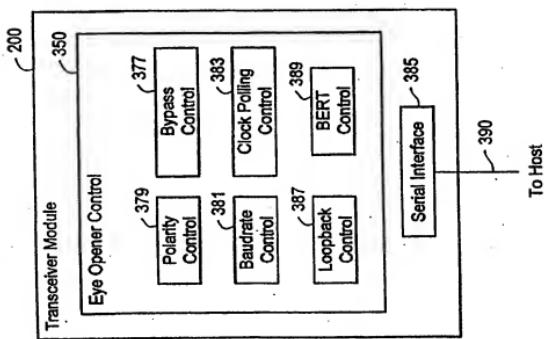


Fig. 7

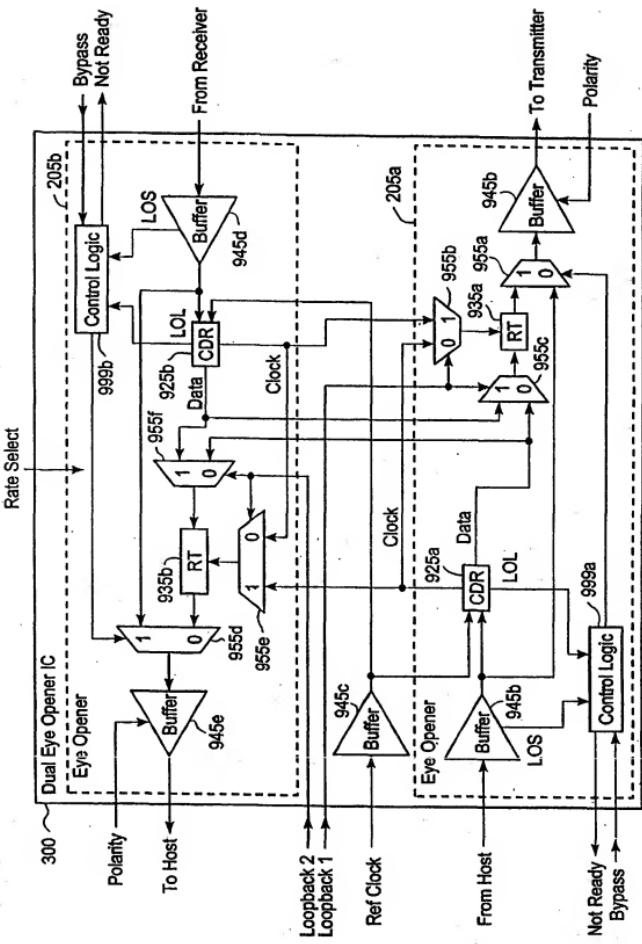


Fig. 8

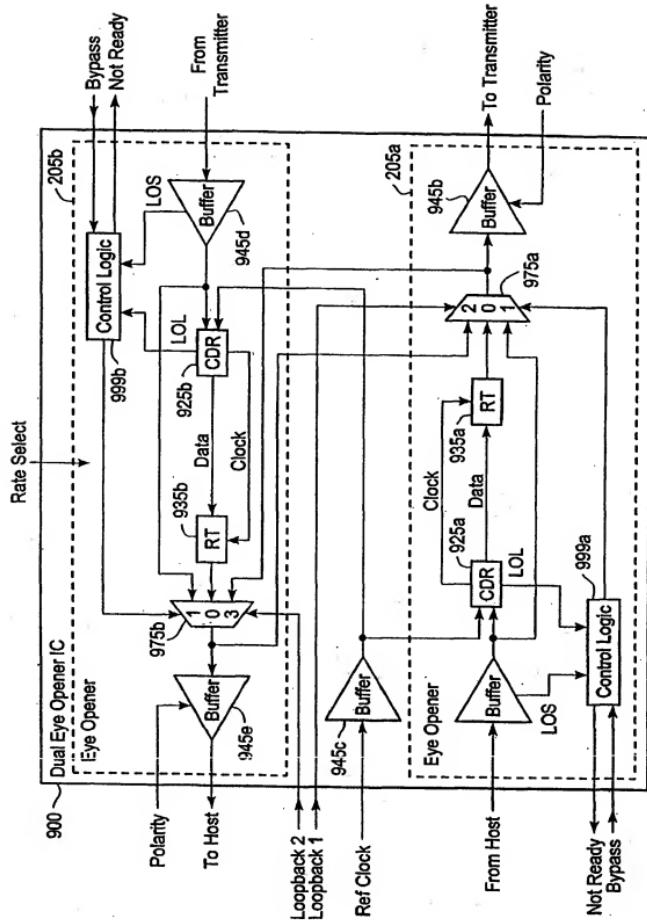


Fig. 1

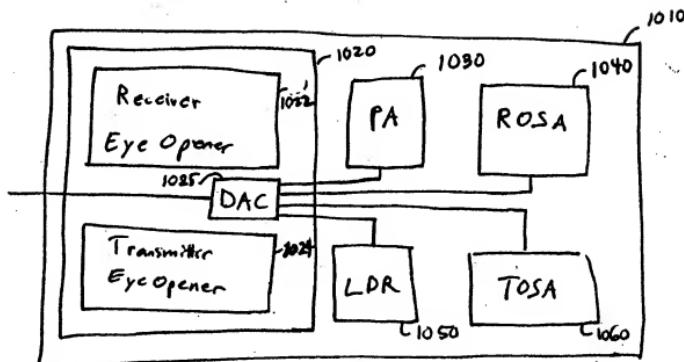


Fig. 10

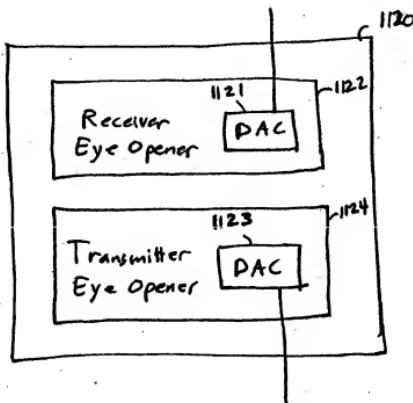


Fig. 11

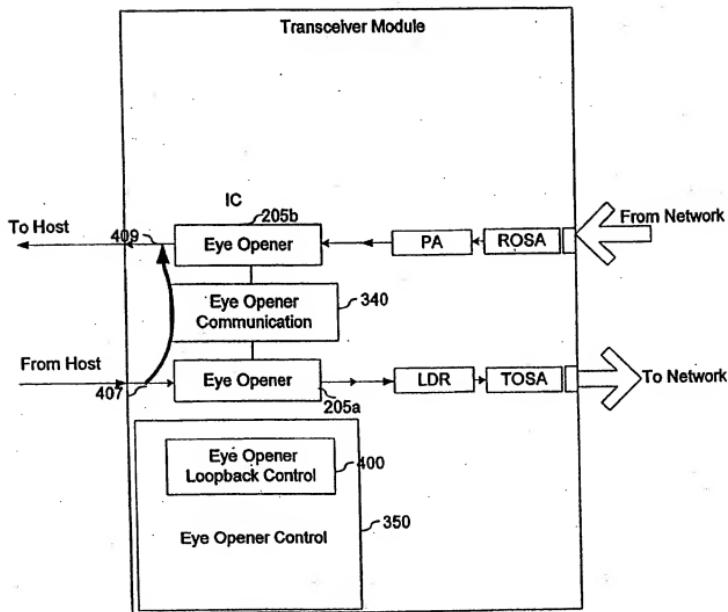


Fig. 12A

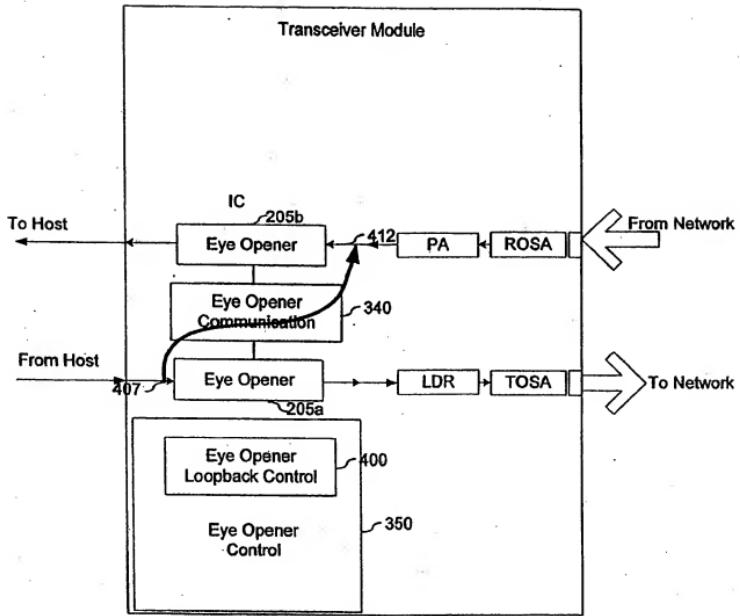


Fig. 12B

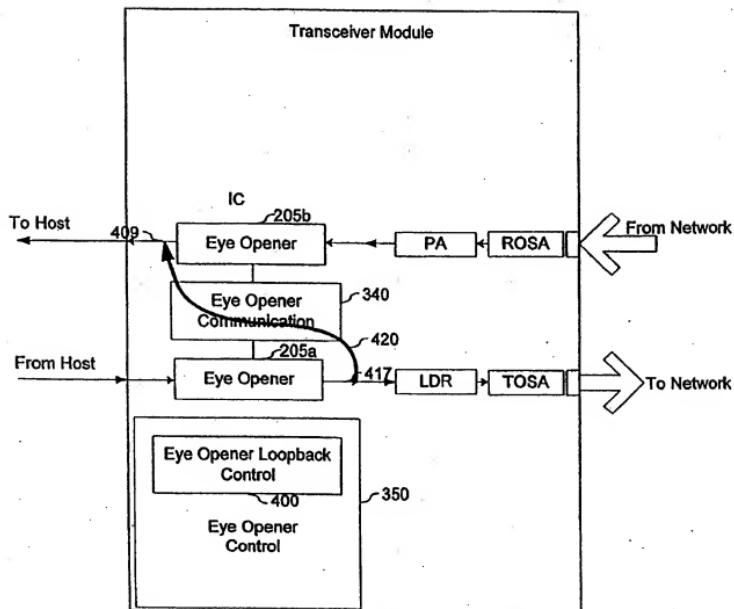


Fig. 12C

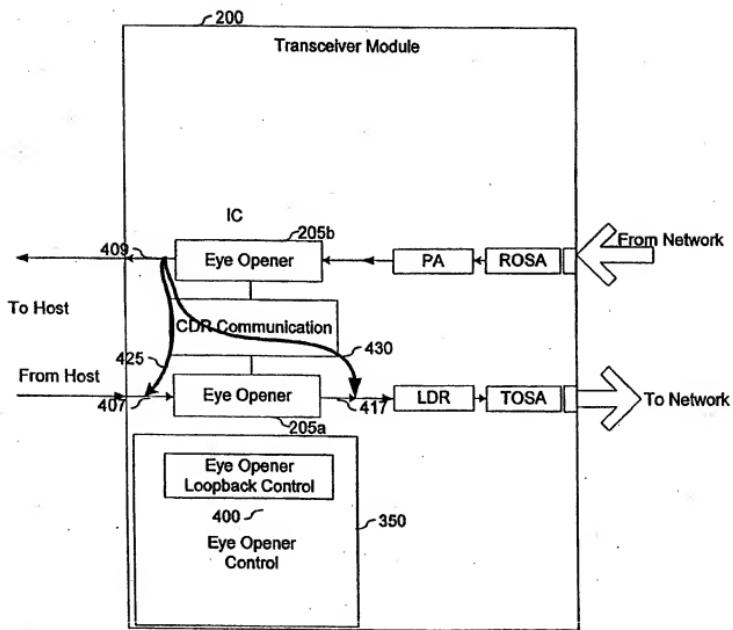


Fig. 12 D

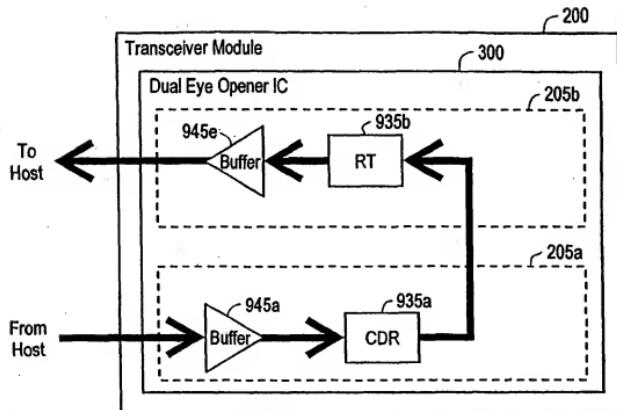


Fig. 13A

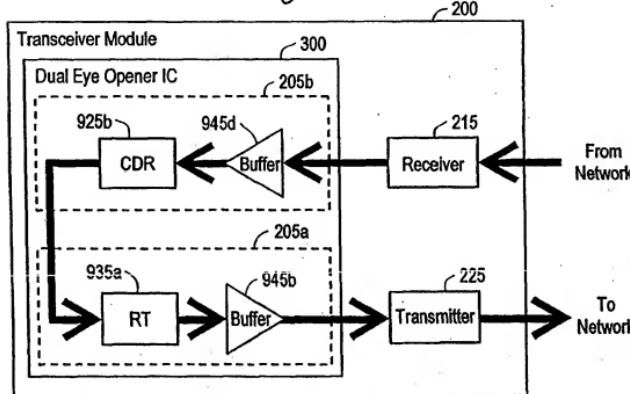


Fig. 13B

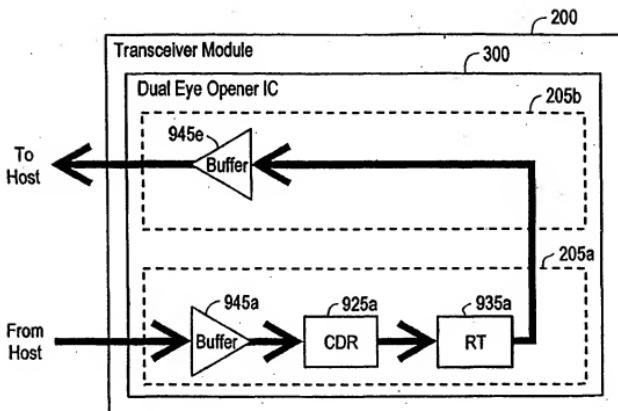


Fig. 13C

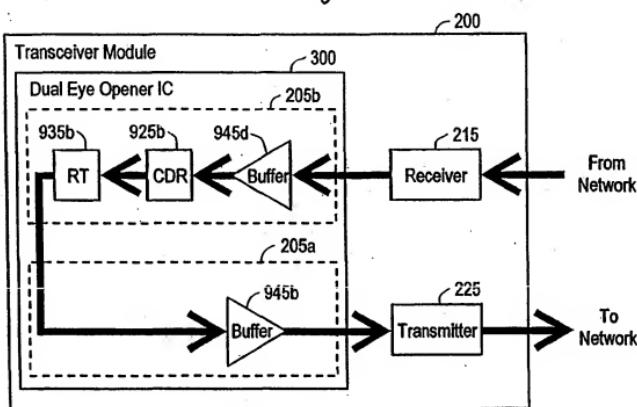


Fig. 13D

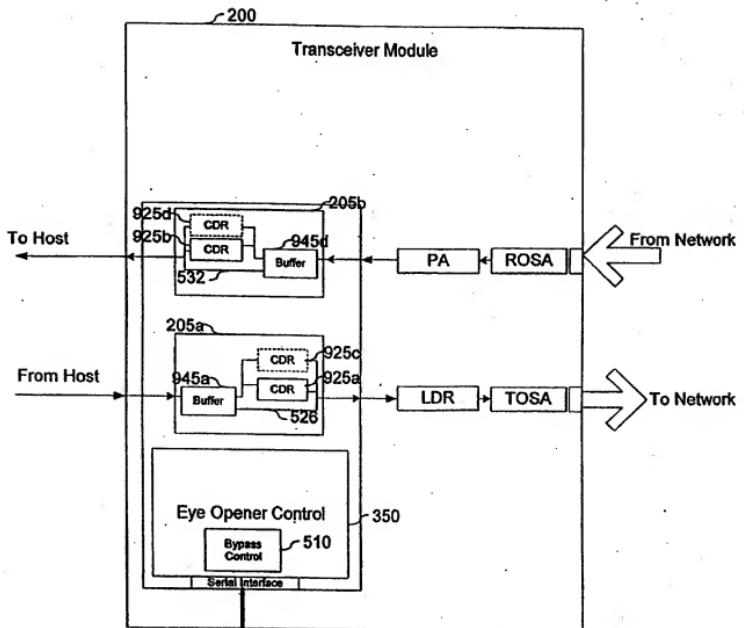


Fig. 14

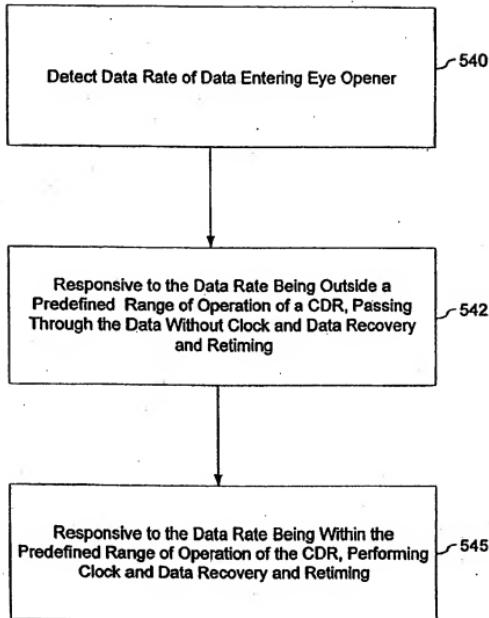


Fig. 15

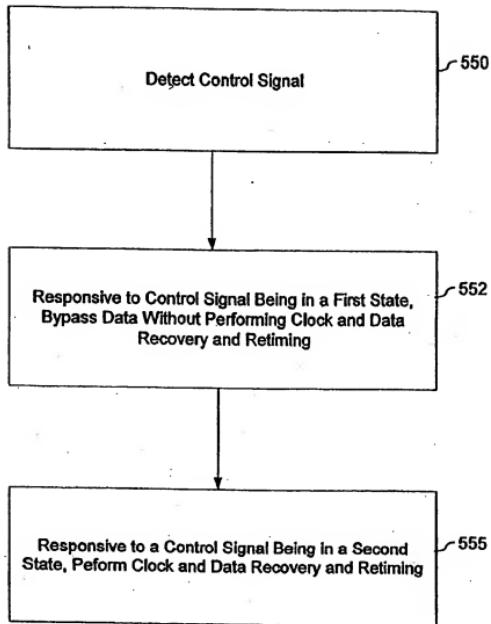


Fig. 16

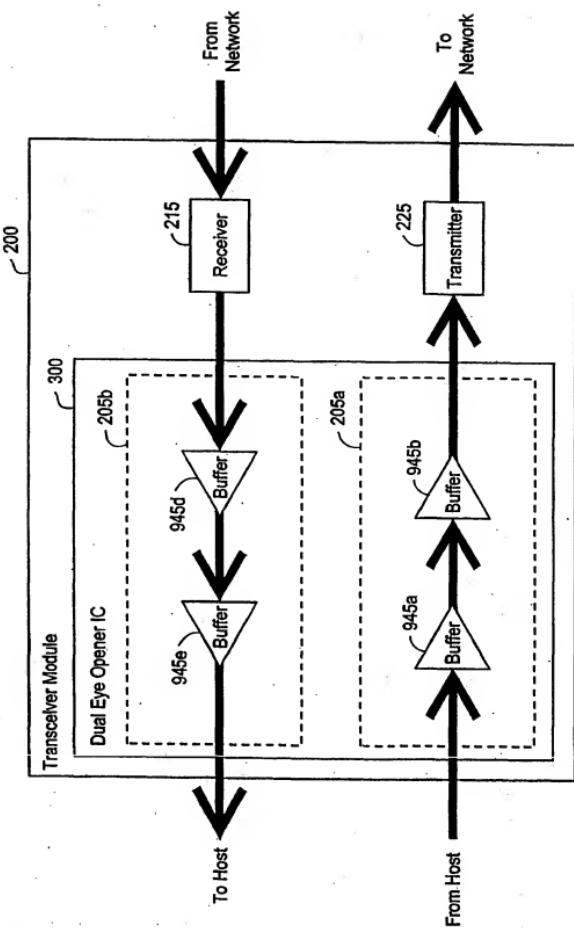


Fig. 17

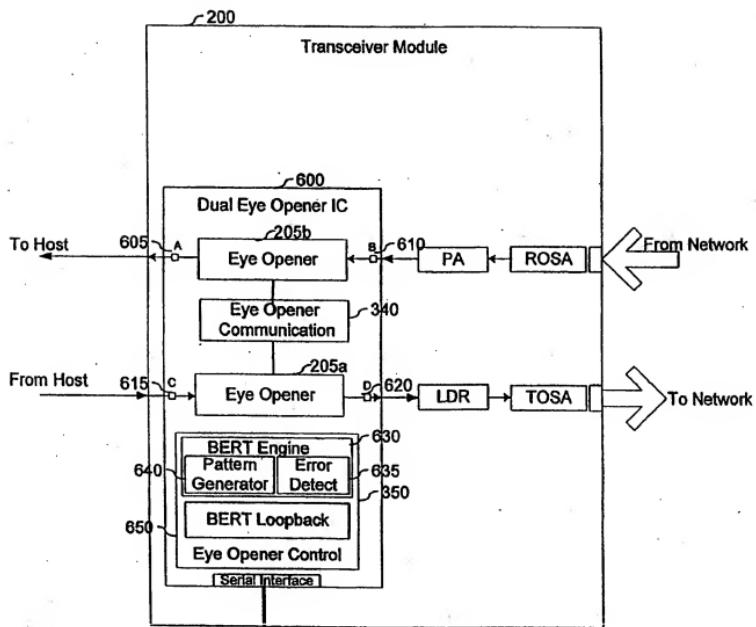


Fig. 18

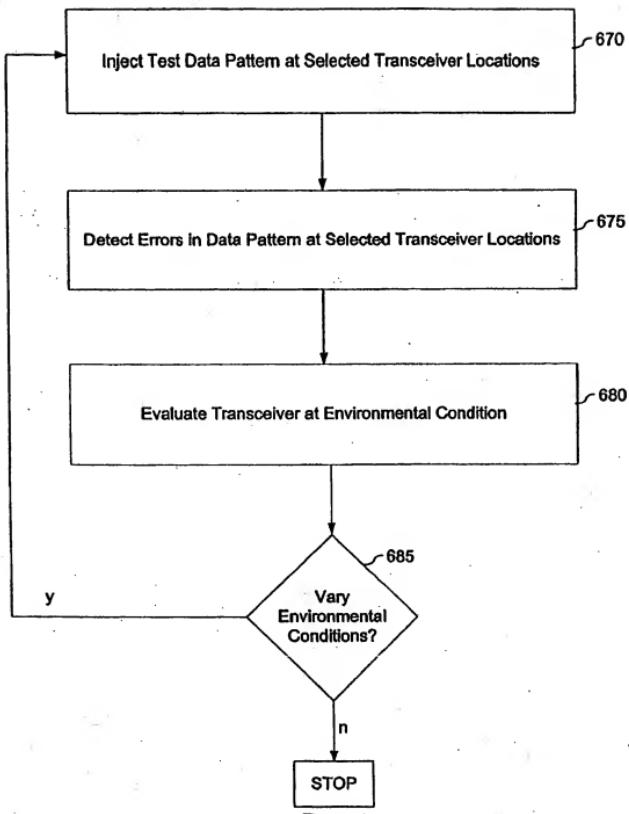


Fig. 19

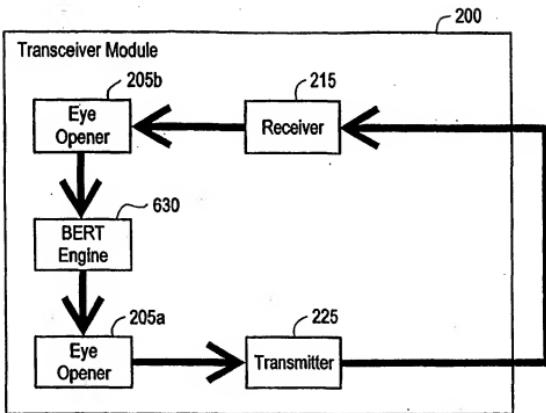


Fig. 20A

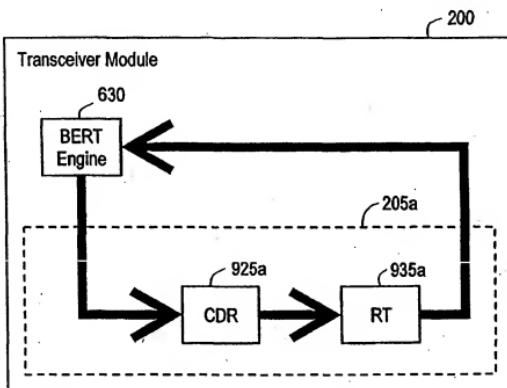


Fig. 20B

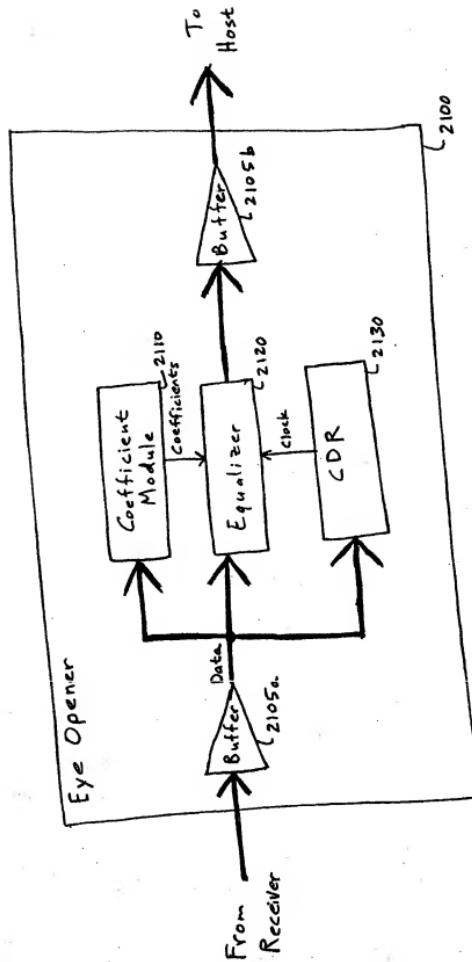


Fig. 21A

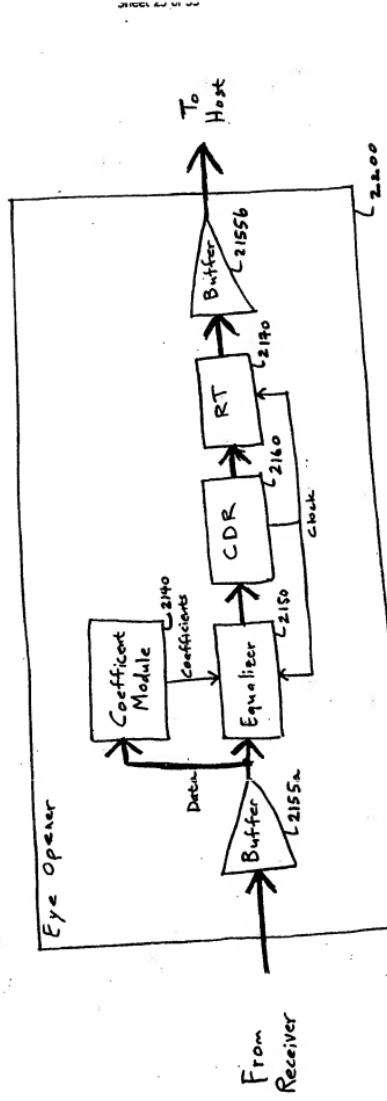


Fig. 21B

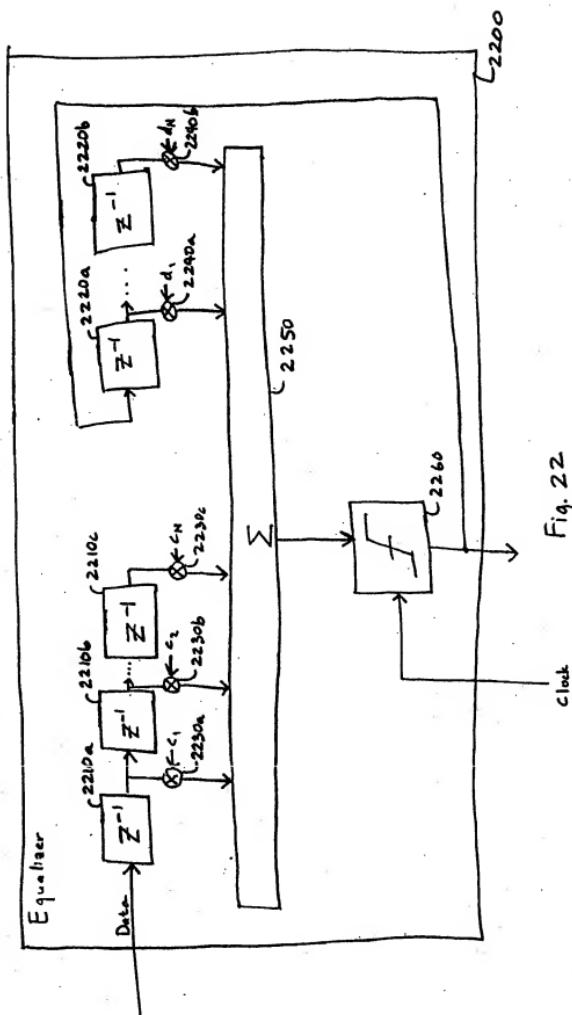


Fig. 2.2

clock

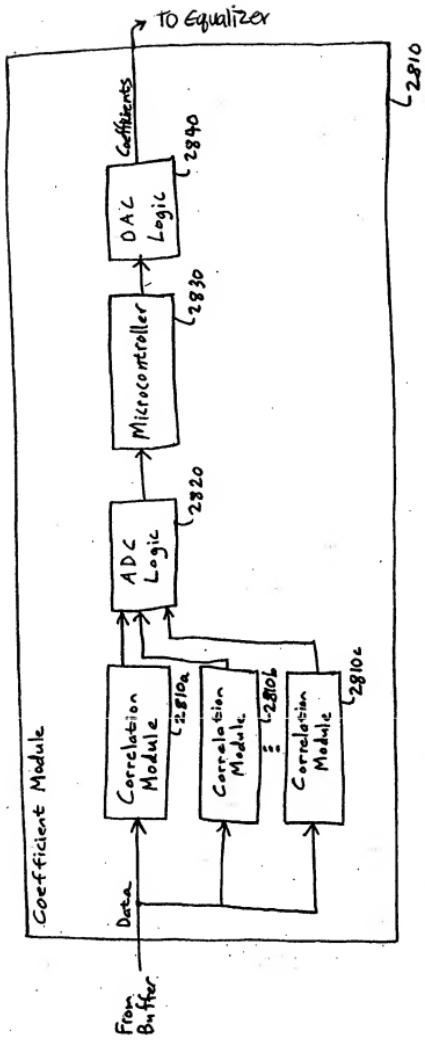
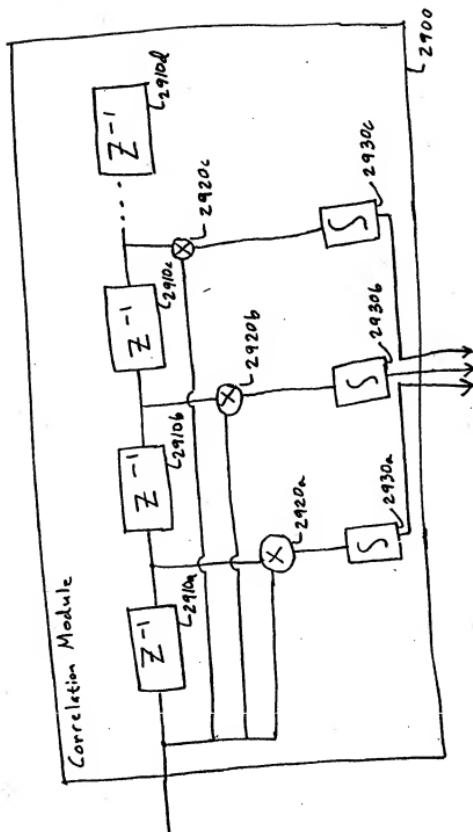


Fig. 23

Fig. 24



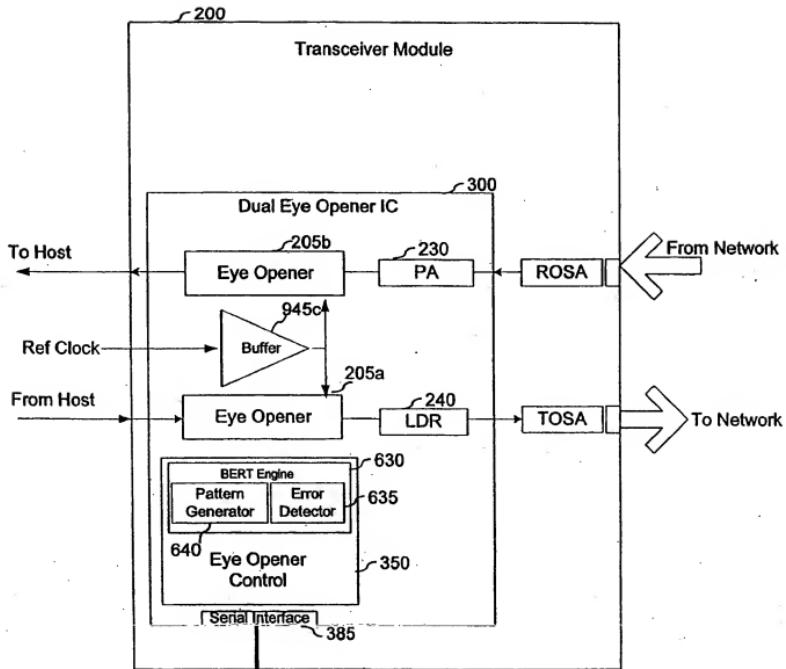


Fig. 25

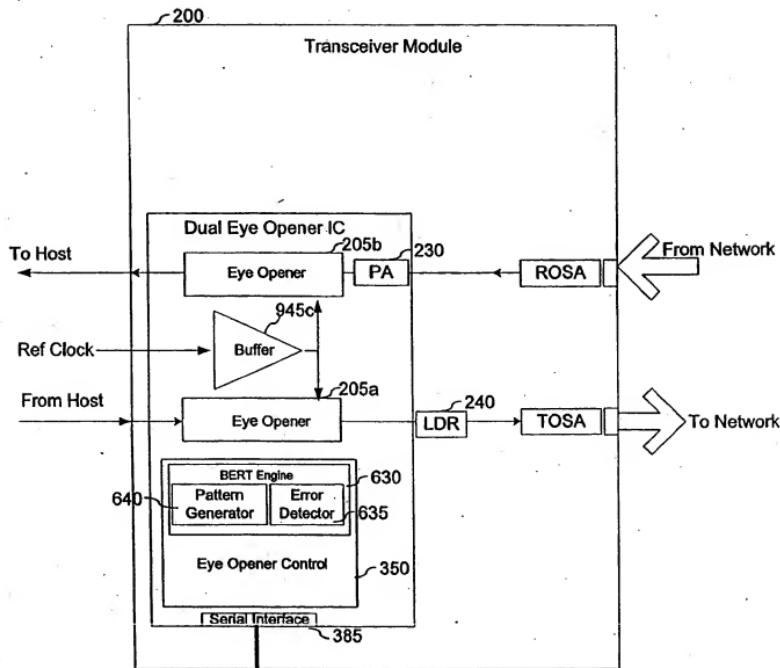


Fig. 26

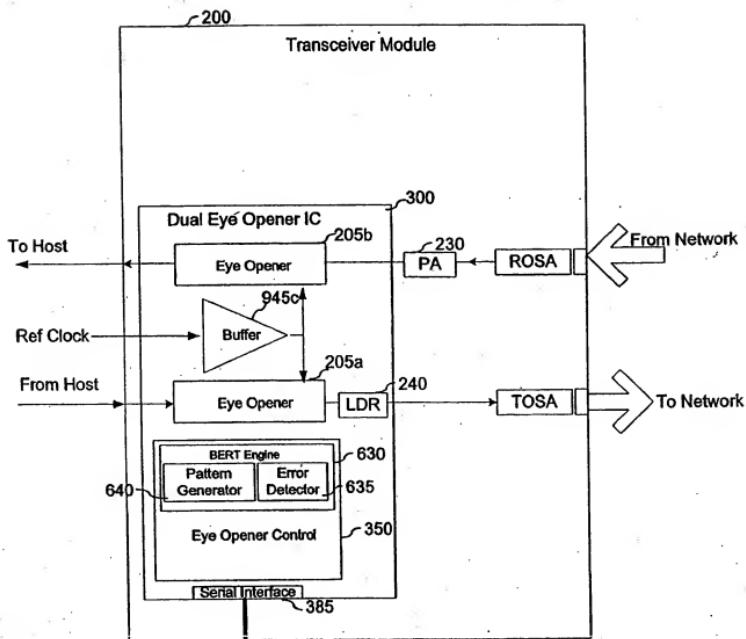


Fig. 27

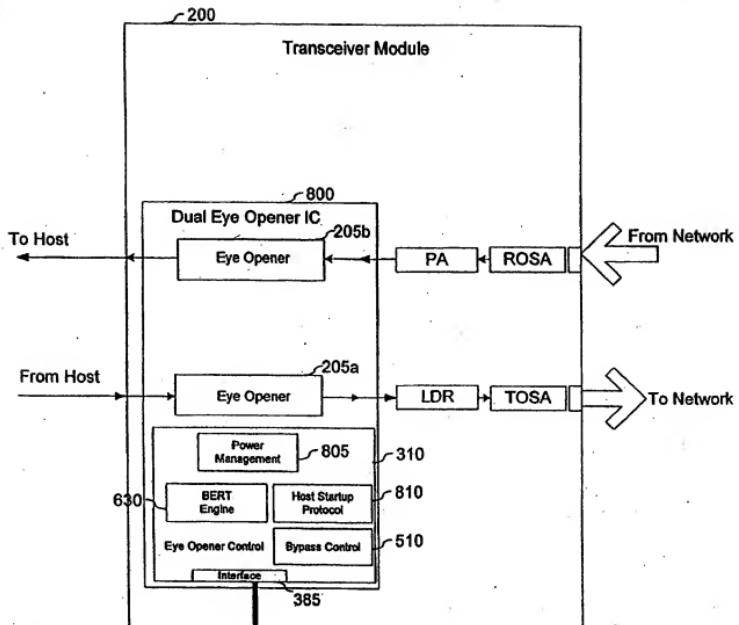


Fig. 28

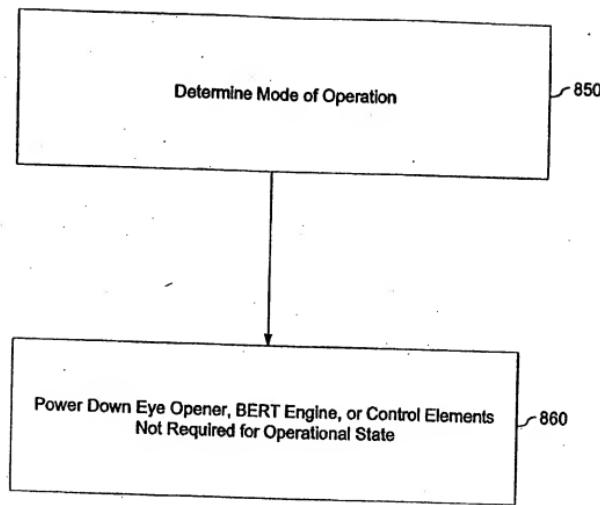


Fig. 29